

Notice of References Cited	Application/Control No. 10/802,577	Applicant(s)/Patent Under Reexamination PLANTS, WILLIAM C.	
	Examiner Gopal C. Ray	Art Unit 2111	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,326,807	12-2001	Veenstra et al.	326/40
*	B	US-6,249,143	06-2001	Zaveri et al.	326/40
*	C	US-6,038,627	03-2000	Plants, William C.	710/100
*	D	US-6,028,809	02-2000	Schleicher et al.	365/230.03
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"SRAM based re-programmable FPGA for space applications" by Wang et al. (abstract only) Publication Date: Dec. 1999
	V	"Intel's FLEXlogic FPGA architecture" by Smith, D.E. (abstract only) Publication Date: 22-26 Feb. 1993
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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